3 Phase Low frequency SCR Transformer Based UPS(10-200KVA)

System Introduction

FC Series is a full digitized DSP controlled online double conversion 3 Phase Industrial UPS. LF transformer isolated all interferences such as Mains Input Surge or Load impact from the UPS, which also greatly reduce the risk of the UPS and load. FC Series has power parallel redundancy feature with real 0.9 output PF. It also contains friendly and intuitive user Interface. It provides reliable, stable, and constant AC power for IT equipment, telecommunication critical devices.



System Features

High Performance Index

- ◆ Advanced SCR Rectifier Technology. With additional accessories, the input PF can be upto 0.99 and Input THDI <4.5%;
- ◆ 5th generation IGBT technology to adapt 380/400/415V, 50/60Hz Mains Grid Supply Systems;
- ◆ Output Power Factor 0.9 to carry 12.5% load capacities than traditional UPS;
- ♦ Wide Input Voltage Range 380Vac (-45% to +25%) with 50/60Hz ±5% Freq. Range; High adaptive capacity and generator Capability;
- ◆ Overall Efficiency upto 90%. Can reach to 98% on ECO Mode;
- ◆ Powerful overload ability with output short circuit protection technology: 110%-120% overloads for 10 minutes.125%-150% for 1 minute;
- ◆ Flexible Battery Configuration. It can be set on the front panel with 28-32 Units of Batteries. Battery Charging Compensation Feature.
- ◆ Intelligent Fault Diagnosis system with large storage memory of fault histories;
- ♦ Friendly and Intuitive User Interface. Large Color Touch LCD Screen with multi-functional buttons.

Safe and Reliable

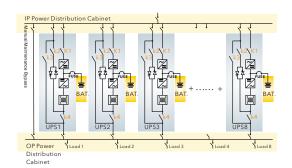
- ◆ DSP technology to control UPS power function processes (including Parallel function) in order to increase system reliability;
- ◆ Flexible Single or Dual Utility Power Input to comprehensively protect load devices;
- ◆ Mis-Phase Connection Diagnosis, Evaluation and Alarm;
- ◆ 100% 3 Phase Unbalanced Allowed:
- ◆ Front Maintenance Design. MTBF can be upto 300K Hours;
- ◆ 90% of system components are from international famous brands. All devices will be aged and tested for more than 24 hours

Application Type

FC Series is designed for many different applications and compatible Loads, such as Data Center, Telecom, Network management center, financial center Security Trading Settlement Center, Banking. Large Theater, Stadium, traffic Administration Bureau, Road and Railroad Tunnel Lightning Control and Monitoring Center, Port Information Center. Semiconductor production line, automatic production line and related device.

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Specification

	FC100L33	FC150L33	FC200L33	FC300L33	FC400L33	FC600L33	FC800L33	FC1000L33	FC1200L33	FC1600L33	FC2000L
Capacity	10KVA	15KVA	20KVA	30KVA	40KVA	60KVA	80KVA	100KVA	120KVA	160KVA	200KVA
Сарасну	9KW	13.5KW	18KW	27KW	32KW	54KW	72KW	90KW	108KW	144KW	180KW
UPS Structure		Online Doubl	ne Specifica e Conversion	ITION							
Appearance	Low Frequency with Output Isolated Transformer										
	22%										
Overall Efficiency (AC-AC)											
Noise (In 2 Meters)		<50-60dB									
Working Temp.		-10-40℃									
Storage Temp.		-25 ~ 60°C (V	Vithout Batter	ies)							
Humidity		< 95%, Non-0	Condensing								
National Standard		EN50091-1/I	EC950								
International Standard	EN50091-1/2; EN62040-1; EN62040-2										
Parallel Redundancy	Available upto 8 units										
Protection	Overload, Short-Circuit, Over Temp., Utility Power Voltage High/low, BAT Voltage High/low										
ECO	Available										
EPO Function	AVAIIADIE AVAIIADIE										
	Available Available (Not Recommended)										
DC Start			it Recommend	iea)							
Generator Compatibility		Available									
Display	5-7 inch LCD color touch screen + LED working indicators including Input/Output Voltage, Frequency, Current, Power, Load Capacity,										
	Serial Number, Operational Mode, Discharge Time, History Logs. All settings including can be done on the front panel, including battery										
		voltage, inpu	t and output v	oltage, freque	ncy, parallels	etting and etc					
Marke		A 4 -									
Mute		Auto									
Cabinet Standard		IP20									
Cooling System			eed Control C	cooling Fan							
Elevation		<1500M, With	nout Derated								
		Rectifier Sp	ecification								
Input Voltage			/ (3 phase + F	DE)							
			(3 phase + r	-=)							
Input Voltage Range		285-475Vac									
Input Frequency Range		45-65Hz									
Input PF		0.99 (with in	put filter)								
THDI		<5% (with op	tional access	ories)							
Dual Input Availability		Available(Op	tional access	ories)							
Input Mis Phase Protection		Misphase Ala	rm, UPS will r	not be started							
Input Phase lost Protection				rk on Bypass r	node						
Soft-Start		> 20 Seconds									
Input Current	23A	31A	39A	54A	70A	100A	125A	160A	192A	256A	320A
input content		output Specif		347	704	100A	1237	TOUR	132A	230A	320A
	,										
		Line Voltage:	380× (1±1%) AC or Phase	Voltage: 220×	(1±1%)AC					
Output Voltage											
Output PF		0.8/0.9 (No Ia	ig)								
		0.8/0.9 (No Ia		; 380Vac±2%	(50-0% Sud	den Change)	; 380Vac±3%	(100-0% Sud	den Change)		
Output PF		0.8/0.9 (No la 380Vac±1%	(Static Load)							±8% or under I	BAT Mode
Output PF Output Voltage Regulation Output Freq		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz:	(Static Load) Online Mode	tracking input	and bypass fr	eq.; ±0.1%: wh				±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear	(Static Load) Online Mode r Full Load),	tracking input <3% (Non-L	and bypass fr inear Full Loa	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase	(Static Load) Online Mode r Full Load), e 100% Unbal	tracking input <3% (Non-L anced, Maxim	and bypass fr inear Full Loa um Synchroni	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase ≤1%(Balance	(Static Load) Online Mode r Full Load), e 100% Unbal ed Load); ≤2%	tracking input <3% (Non-L anced, Maxim (50% Balance	and bypass fr inear Full Loa um Synchroni d Load)	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase ≤1%(Balance ≤1°(Balance	(Static Load) Online Mode r Full Load), e 100% Unbal ed Load); ≤2%	tracking input <3% (Non-L anced, Maxim	and bypass fr inear Full Loa um Synchroni d Load)	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Lineal Allow 3 Phase ≤1%(Balance ≤1°(Balance 45-65Hz	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5	tracking input <3% (Non-L anced, Maxim (50% Balance	and bypass fr inear Full Loa um Synchroni d Load)	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase ≤1%(Balance ≤1°(Balance	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5	tracking input <3% (Non-L anced, Maxim (50% Balance	and bypass fr inear Full Loa um Synchroni d Load)	eq.; ±0.1%: wh d)	hen input or b			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Lineal Allow 3 Phase ≤1% (Balance ≤1° (Balance 45-65Hz Pure Sine Wa	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5	tracking input <3% (Non-L anced, Maxim (50% Balance	and bypass fr inear Full Loa um Synchroni d Load) Load)	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase ≤1% (Balance ≤1° (Balance 45-65Hz Pure Sine Wa >125%: More	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced	and bypass fr inear Full Loa um Synchroni d Load) Load)	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Linear Allow 3 Phase ≤1% (Balance ≤1° (Balance 45-65Hz Pure Sine Wa >125%: More 3:1, Dynami	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5 ave e than 10 mins ic Response 3	tracking input <3% (Non-L anced, Maxim (50% Balanced 0% Balanced ; > 150%: Mc cycles at 90%	and bypass fr inear Full Loa um Synchroni d Load) Load) ure than 60s tr s static value	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±1% (Linear Allow 3 Phase ≤1% (Balancec 45-65Hz Pure Sine Wa 3:1, Dynami Circuit Auto-F	(Static Load) Online Mode r Full Load), e 100% Unbal id Load); ≤2% d Load); ≤2°(5 ave than 10 mins ic Response 3 Protection, By	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced ; > 150%: Mo t cycles at 90% pass Switch T	and bypass fr inear Full Loa um Synchroni d Load) Load) ure than 60s tr s static value	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
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Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: < 1% (Lineai Allow 3 Phase ≤1% (Balancec 45-65Hz Pure Sine Wa >125%: More Circuit Auto-FINV. Output A Byposs Spe	(Static Load) Online Mode Full Load), eller Full Load), eller Full Load), eller Full Load), eller Full Load); eller Full Load); eller Full Load); eller Full Load); eller Full Full Full Full Full Full Full Ful	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced ; > 150%: Mo t cycles at 90% pass Switch T	and bypass fr inear Full Loa um Synchroni d Load) Load) ure than 60s tr s static value	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
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Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±1% (Linear Allow 3 Phass ≤1% (Balancec 1° (Balancec 45-65Hz Pure Sine Wa 3:1, Dynami Circuit Auto-FINV. Output A Bypass Spe 0ms 380Vac (-15 50/60Hz±1Hz	(Static Load) Online Mode r Full Load) , r = 100% Unbal dd Load); ≤2% dd Load); ≤2°(5 ave than 10 mins dc Response 3 Protection, By uuto-Locked P Cification ~+15%)	tracking input <3% (Non-L anced, Maxim (50% Balanced) % Balanced ; > 150%: Mc cycles at 90% pass Switch Trotection	and bypass fr inear Full Loa um Synchroni d Load) Load) ure than 60s tr s static value	eq.; ±0.1%: wi d) zation ±1 Hz/s	hen input or by			±8% or under l	BAT Mode
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Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift requency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range ypass> INV Transfer Time Frequency Tracking Speed anual Maintenance Bypass Charging Methods		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±1% (Lineau Allow 3 Phases ±1% (Balance ±1° (Balance ±1° (Balance Torus 125%: More 3:1, Dynamic Circuit Auto-Finv. Output A Bypass Spe 0ms 380Vac (-15 50/60Hz±1Hz 2ms 0.5-2hz/s Available Battery Spe DSP Controll Sealed Lead.	(Static Load) Online Mode r Full Load) , 1 = 1100% Unbal d Load); ≤2°(5 d Load);	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced ; > 150%: Mc cycles at 90% pass Switch T rotection Adjustable	and bypass frinear Full Loa um Synchroni d Load) Load) wre than 60s tr. static value ripping	eq.; ±0.1%; wl d) zation ±1 Hz/s ansfer to bypa at Charge, Int	nen input or by	pass frequence	ey is more than	±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range ypass> INV Transfer Time Frequency Tracking Speed Ianual Maintenance Bypass Charging Methods Type Rated Volts/Units		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±1% (Lineau Allow 3 Phases ±1% (Balance ±1° (Balance ±1° (Balance Torus 125%: More 3:1, Dynamic Circuit Auto-Finv. Output A Bypass Spe 0ms 380Vac (-15 50/60Hz±1Hz 2ms 0.5-2hz/s Available Battery Spe DSP Controll Sealed Lead.	(Static Load) Online Mode r Full Load) , 1 = 1100% Unbal d Load); ≤2°(5 d Load);	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced) ; > 150%: Mc cycles at 90% pass Switch T rotection Adjustable	and bypass frinear Full Loa um Synchroni d Load) Load) wre than 60s tr. static value ripping	eq.; ±0.1%; wl d) zation ±1 Hz/s ansfer to bypa at Charge, Int	nen input or by	pass frequence	ey is more than	±8% or under l	BAT Mode
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Output PF Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range ypass> INV Transfer Time Frequency Tracking Speed Ianual Maintenance Bypass Charging Methods Type Rated Volts/Units Float Charge Voltage Charging Current		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±1% (Lineau Allow 3 Phase ±1% (Balance ±1° (Balance ±1° (Balance 10 Fine Ware 125%: More 3:1, Dynami 10 Fine Ware 125%: More 125%:	(Static Load) Online Mode r Full Load), e 100% Unbal dd Load); ≤2% d Load); ≤2°(5 ave e than 10 mins ic Response 3 Protection, By nuto-Locked P cification -+15%) z, ±2Hz, ±3Hz ccification ed Charger: E Acid Maintene Std. for 32 Un om the front Si	tracking input <3% (Non-L anced, Maxim (50% Balanced) ; > 150%: Mc cycles at 90% pass Switch T rotection Adjustable Equalized/Puls ance Free hits, adjustab	and bypass frinear Full Loa um Synchroni d Load) Load) ure than 60s tr. static value ripping e Charge, Flo	eq.; ±0.1%; wl d) zation ±1 Hz/s ansfer to bypa at Charge, Int	elligent Batter	ry Managemen	ey is more than	±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range ypass> INV Transfer Time Frequency Tracking Speed anual Maintenance Bypass Charging Methods Type Rated Volts/Units Float Charge Voltage		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±8% at 50Hz: <1% (Lineau Allow 3 Phasts 1% (Balancet 45-65Hz Pure Sine Wa >125%: More 3:1, Dynamic Circuit Autor INV. Output A Bypass Spe 0ms 380Vac (-15 50/60Hz±1Hz 2ms 0.5-2hz/s Available Battery Spe DSP Controll Sealed Lead. 12V/384Vdc, 438Vdc Can be set fre BAT over-cha	(Static Load) Online Mode r Full Load), e 100% Unbal d Load); ≤2% d Load); ≤2°(5 ave than 10 mins ic Response 3 Protection, By tuto-Locked P cification ed Charger: E Acid Maintena Std. for 32 Un om the front Si arged, Emerged	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced ; > 150%: Mc cycles at 90% pass Switch T rotection Adjustable Equalized/Puls ance Free nits, adjustab creen encycharger s	and bypass frinear Full Loa um Synchroni d Load) Load) ure than 60s tr. static value ripping e Charge, Flo	eq.; ±0.1%; wl d) zation ±1 Hz/s ansfer to bypa at Charge, Int	elligent Batter	ry Managemen	ey is more than	±8% or under l	BAT Mode
Output PF Output Voltage Regulation Output Voltage Regulation Output Freq THD 3 Phase Unbalanced Output Volt. Unbalanced Input/Output Phase Swift Frequency Tracking Range Output Waveform Overload Crest Ratio Short-Circuit Output Abnormal Static Bypass Transfer Time Static Bypass Input Range Frequency Range ypass> INV Transfer Time Frequency Tracking Speed Ianual Maintenance Bypass Charging Methods Type Rated Volts/Units Float Charge Voltage Charging Current Abnormal Protections		0.8/0.9 (No la 380Vac±1% ±8% at 50Hz: ±8% at 50Hz: <1% (Lineau Allow 3 Phast ≤1% (Balance ≤1* (Balance 45-65Hz Pure Sine Wa >125%: More 3 : 1, Dynami Circuit Auto-Fin. W. Output A Bypuss Spe Oms 380Vac (-15 50/60Hz±1Hz 2ms 0.5-2hz/s Available Battery Spe DSP Controll Sealed Lead. 12V/384Vdc, 438Vdc Can be set fire BAT over-chat Communication.	(Static Load) Online Mode r Full Load), 1 100% Unbal d Load); ≤2% d Load); ≤2°(5 ave than 10 mins ic Response 3 Protection, By auto-Locked P Cification ed Charger: E Acid Maintene Std. for 32 Un pm the front S arged, Emerge cific Specific Spec	tracking input <3% (Non-L anced, Maxim (50% Balance 0% Balanced) ; > 150%: Mo coycles at 90% pass Switch Ti rotection Adjustable Equalized/Puls ance Free hits, adjustab creen ency charger s cation	and bypass frinear Full Loa um Synchroni d Load) Load) re than 60s tr. static value ripping e Charge, Flo le from 28-32 hutdown prote	eq.; ±0.1%; wl d) zation ±1 Hz/s ansfer to bypa at Charge, Int	elligent Batter	ry Managemen	ey is more than	±8% or under l	BAT Mode
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Note Specifications are subject to change without further notice.













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